



J3, D1, R1, R2, U2, U5, U6: Do Not Install.

UPDATES Rev 1 board Nov. 7, 2006

- Added FPGA drive of LOSext, CMUFreqSel, DLOSDis, OOF, CDRDis, STS12/STS3, CDRRefSel & CDRAuxRefClk pins of 91L30
- Remove RxLOS from U14 pin 33 (LOSExt)
- Monitor RxLOS with FPGA
- Add SMB's to drive MCLK & Tx51_19M pins of 86SH221
- Added 8 test points to micro-p interface
- Changed Value & Reference definitions for JoTx & JoRx SMB's on page 3

UPDATES Rev 2 board April 23, 2007

- Added Ground pins and VDD pin to XRTSH221 package that were not shown on previous revision of schematic.



